

Datasheet for #sb27Recommendations:

Please read the starter kit user manual (at least installation chapter 5), if available, and have a look at the FAQ at <http://www.alpeslasers.ch/alfaqa.pdf>

WARNING: Operating the laser with longer pulses, shorter period, or higher voltage or current than specified in this document may cause damage and will result in loss of warranty, unless agreed upon with Alpes Lasers!

WARNING: Beware of the polarity of the laser. This laser has to be powered with negative bias on the laser contact (= bonding pad, corresponding to the label "laser" on the LLH) and the positive bias on the base contact (= submount, corresponding to the label "base" on the LLH).

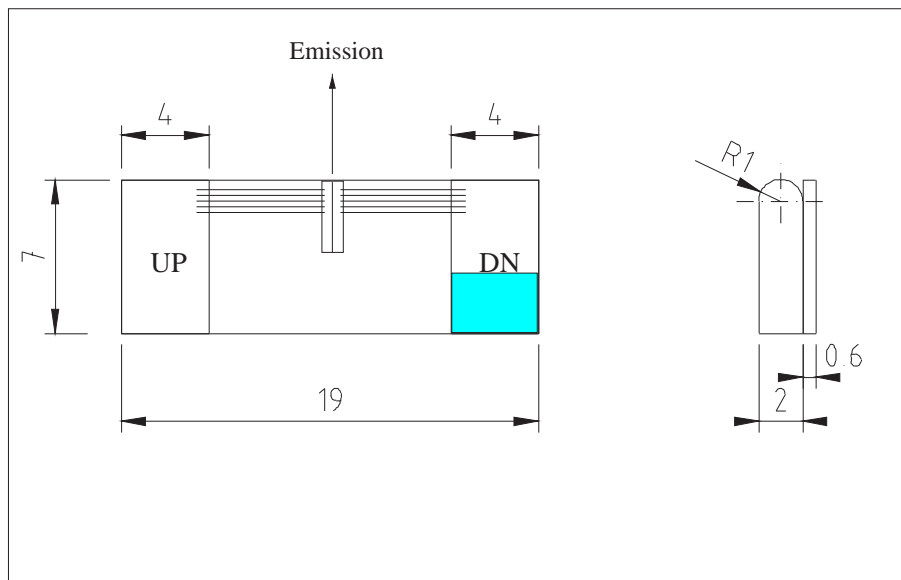


Figure 1: Support mounting for #sb27 (please note that the laser is connected to the DN pad drawn in blue)

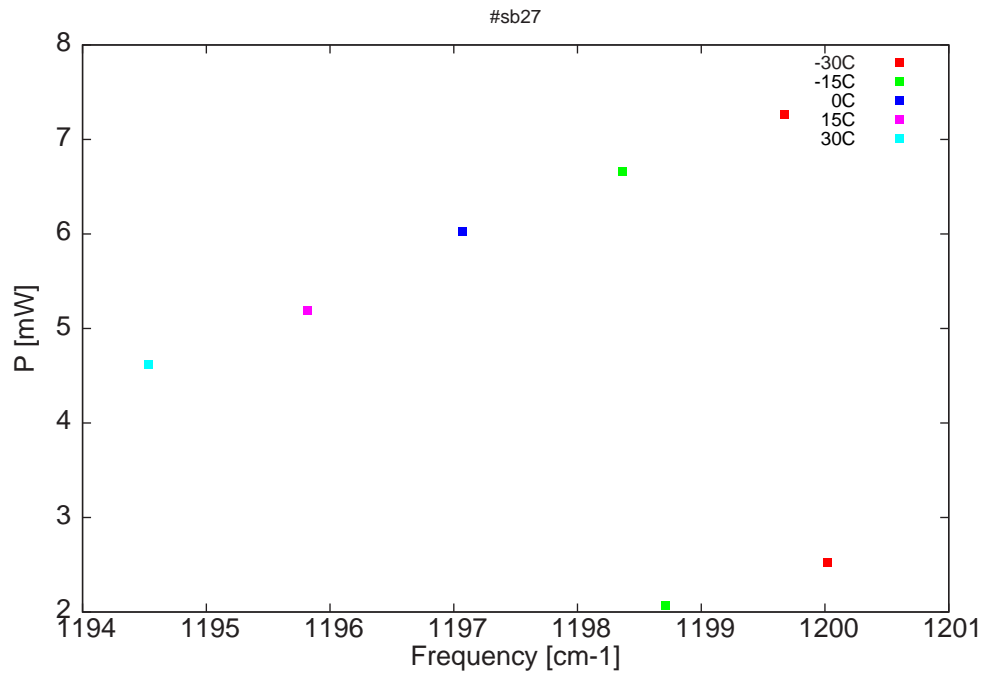


Figure 2: Output power as a function of the singlemode emission frequencies and temperatures

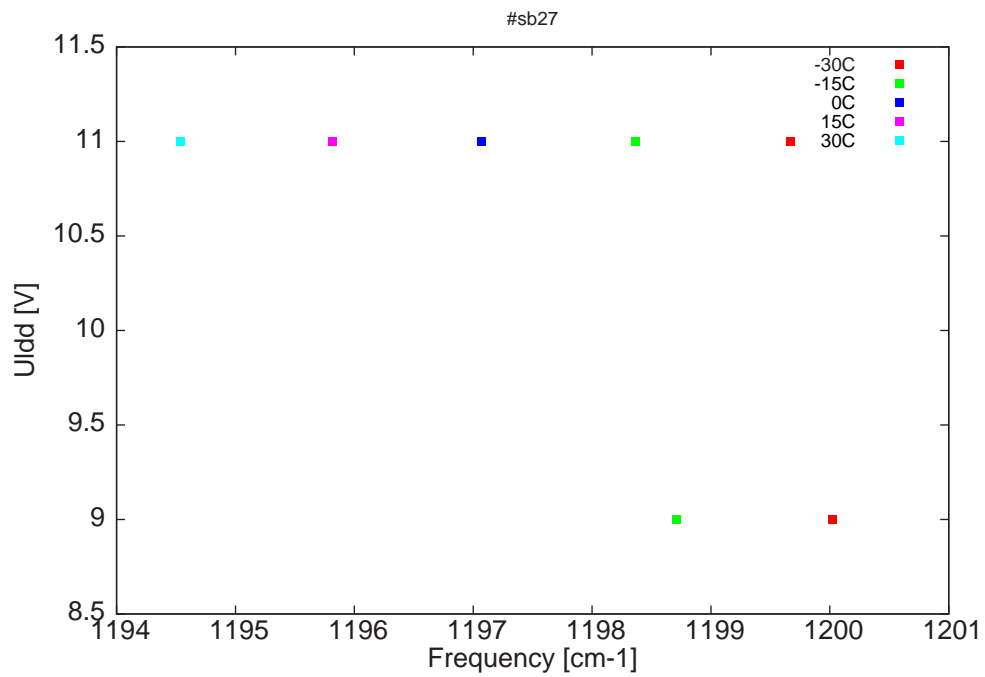


Figure 3: DC voltage fed to LDD (U_{ldd}) as a function of the singlemode emission frequencies and temperatures

λ [nm]	ν [cm ⁻¹]	P[mW]	Temp[°C]	U_{LDD} [V]	I_{pulse} [A]
8333.2	1200	2.5	-30	9	1.84
8335.6	1199.7	7.3	-30	11	2.63
8342.3	1198.7	2.1	-15	9	1.88
8344.7	1198.4	6.7	-15	11	2.67
8353.8	1197.1	6	0	11	2.69
8362.5	1195.8	5.2	15	11	2.7
8371.4	1194.5	4.6	30	11	2.72

Table 1 : singlemode optical output power as function of operating parameters

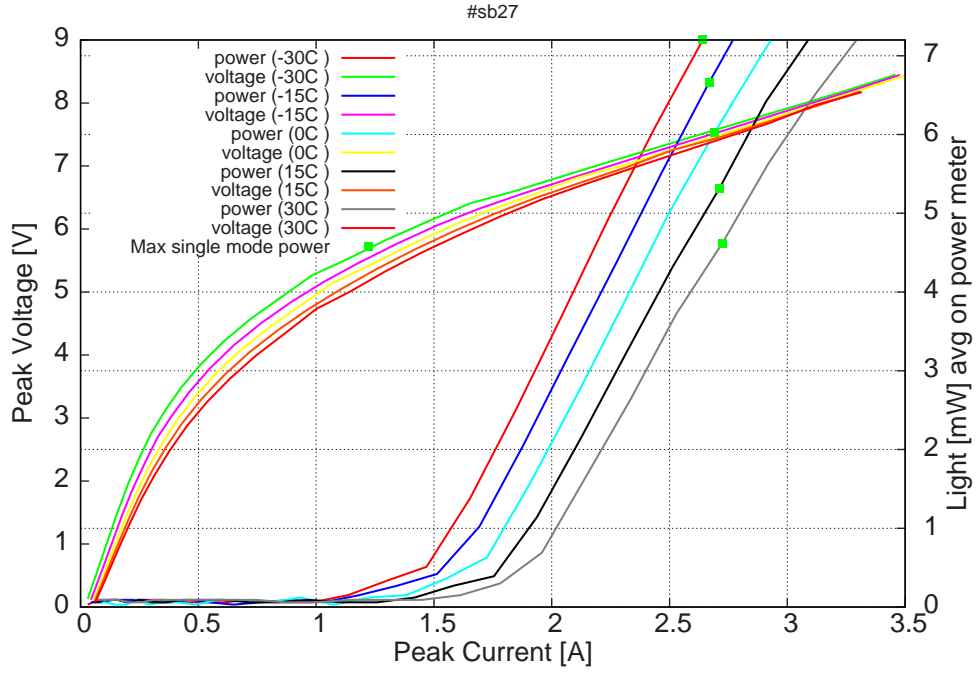


Figure 4: peak voltage and avg power vs peak current (the solid squares indicate the maximum singlemode emitted power)

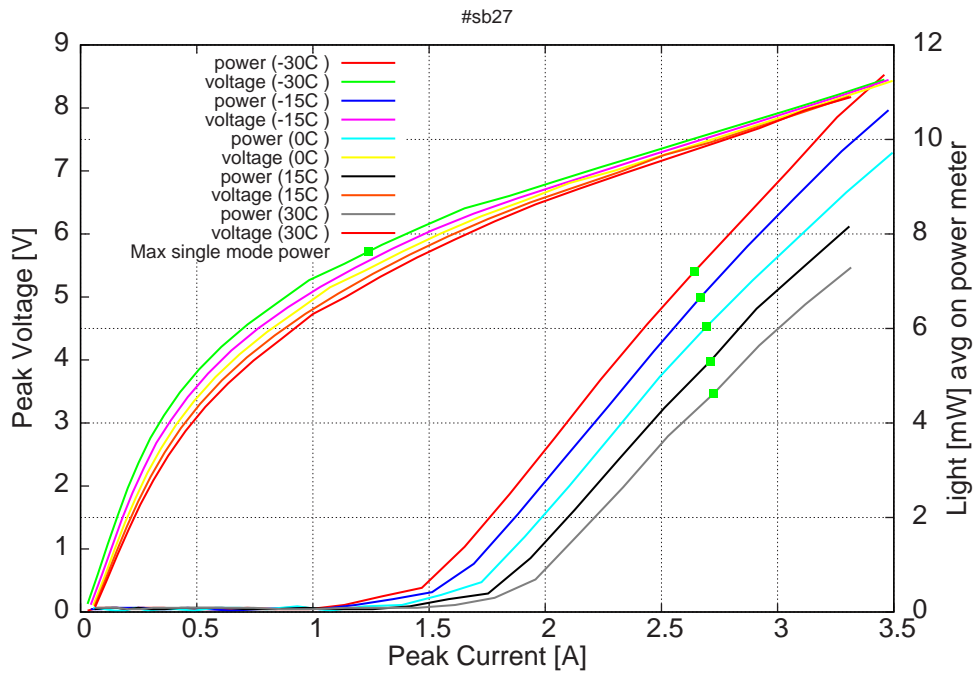


Figure 5: peak voltage and avg power vs peak current (including the multimode region)

Note: data taken with 50ns pulses, $2.5\mu s$ period.
 Laser has small sidemode at highest power.

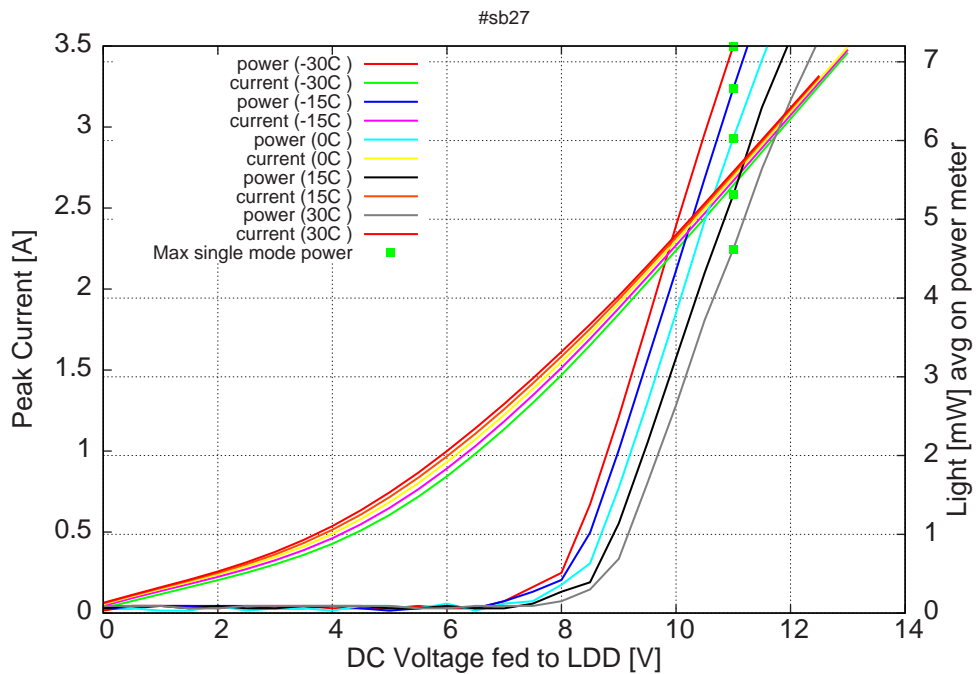


Figure 6: peak current and avg power vs LDD voltage (the solid squares indicate the maximum singlemode emitted power)

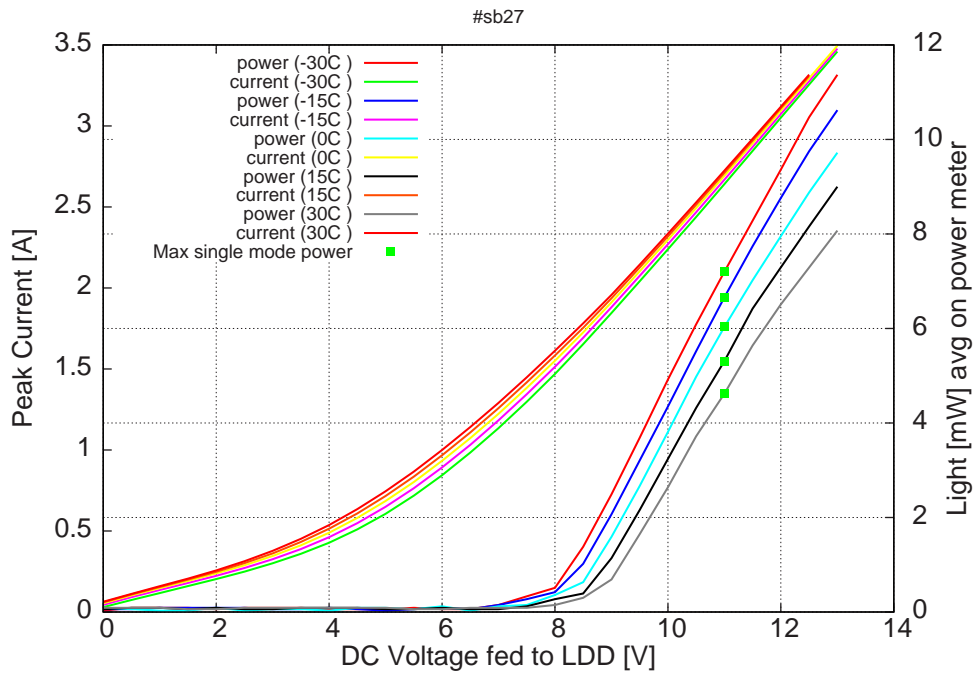


Figure 7: peak current and avg power vs LDD voltage (including the multimode region)

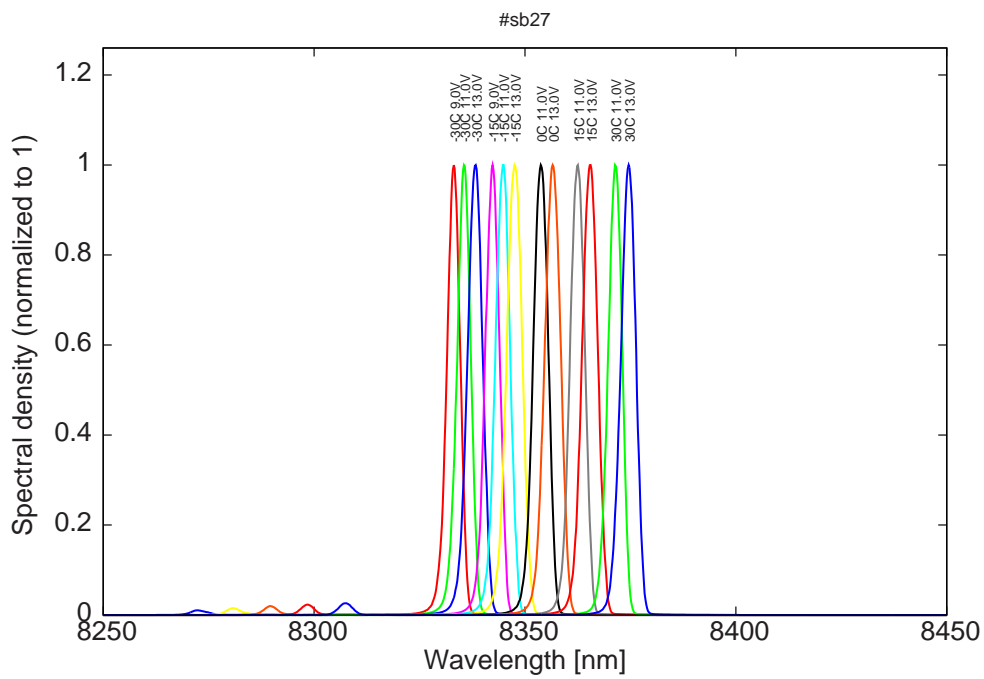


Figure 8: spectra for various temperatures and LDD voltages